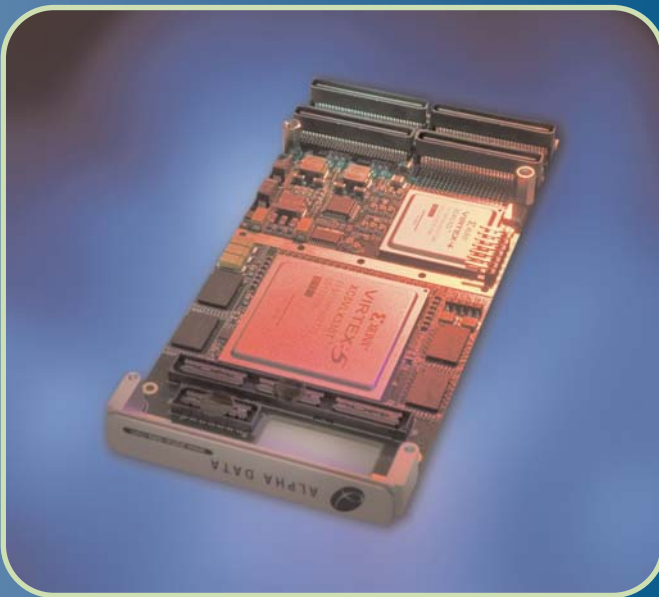




XILINX VIRTEX-5 PMC

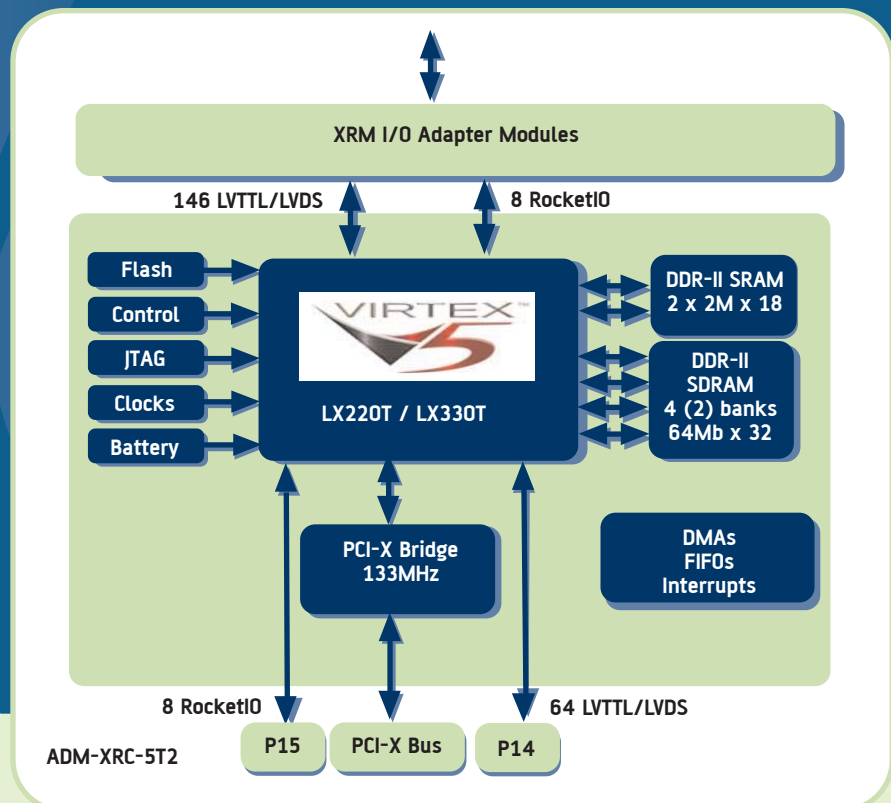


Features

The ADM-XRC-5T2 is a high performance reconfigurable PMC/XMC based on the Xilinx Virtex-5LXT range of Platform FPGAs.

Features include:

- * Support for the Virtex5 LX220T and LX330T devices
- * 133MHz PCI-X or 66MHz PCI host interface with 4 DMA channels
- * 2 or 4 independent banks of DDR-II SDRAM
- * 2 independent banks of DDR-II SSRAM
- * Flexible I/O options via front panel, P14 and P15
- * Programmable Clocks
- * Temperature monitoring
- * Comprehensive cross-platform API with support for Windows, Linux and VxWorks
- * Conduction Cooled option





XILINX VIRTEX-5 PMC

Specifications

XRM-Adapters

XRM-IO146	High Density LVDS/LVTTL
XRM-IO34	Low Density LVDS/LVTTL
XRM-DDR	1 bank DDR SDRAM,
XRM-ZBT	2 banks ZBT SSRAM, RS-232
XRM-FPDP	Front Panel Data Port
XRM-MDR26	MDR IO module
XRM-ADC	1/2 ch 105MSPS 14-bit ADC
XRM-DAC	2 ch 105MHz 14-bit DAC
XRM-ETH	Ethernet, LDVS/LVTTL, RS-232
XRM-CLINK	CameraLink
XRM-HSDI	HD-SDI input and output
XRM-HSSDC-2	Quad Copper RocketIO
XRM-OPT/4	Quad Optical RocketIO
XRM-VIDEO-IO	S-Video, SVGA

Ordering Information

ADM-XRC-5T2(cc)/xxx-yy/(I/O)

cc	Air / Conduction cooled	blank, -CC
xxx	Virtex-5 device	LX220T, 330T
yy	Virtex-5 speed	1 or 2
IO	Front panel adapter	XRM option

	ADM-XRC-5T2
FPGA	Xilinx Virtex-5 LX220T/LX330T FFG1738 footprint
PCI Bus	133MHz PCI-X or 66MHz PCI bridge with 80 MHz, 64-bit local bus. 4 DMA controllers
Memory	SDRAM: 1GByte in 4 banks (2 banks for the LX220T version) of DDR-II SDRAM 2 x 64M x 32-bits 267MHz SSRAM: 8MB in 2 independent banks of DDR-II SSRAM 2M x 18-bits @ 200MHz
Front I/O	Flexible configuration via a range of XRM front panel adapters 146 parallel I/O 8 RocketIO
Rear I/O	64 I/O connections via PMC P14 connector
XMC I/O	8 RocketIO via P15 connector
Clocks	Local bus programmable up to 80MHz low-jitter user clock, programmable up to 637.5MHz Custom clock inputs available through XRM interface
Configuration	PCI Bus direct to SelectMAP port From Flash direct on power up External JTAG connector
Encryption	Dual battery back-up for encryption keys via XRM
Software	Drivers for WinNT, 2000, XP, Linux and VxWorks API with template designs in VHDL and Verilog
Environmental (air cooled option)	Operating temperature 0 to +55°C Non-operating (storage) temperature -40 to +65°C Operating humidity 5% to 90% at 40°C non-condensing Non-operating (storage) humidity 5% to 90% at 40°C non-condensing
EMC	FCC 47CFR Part 2 EN55022 Equipment Class B