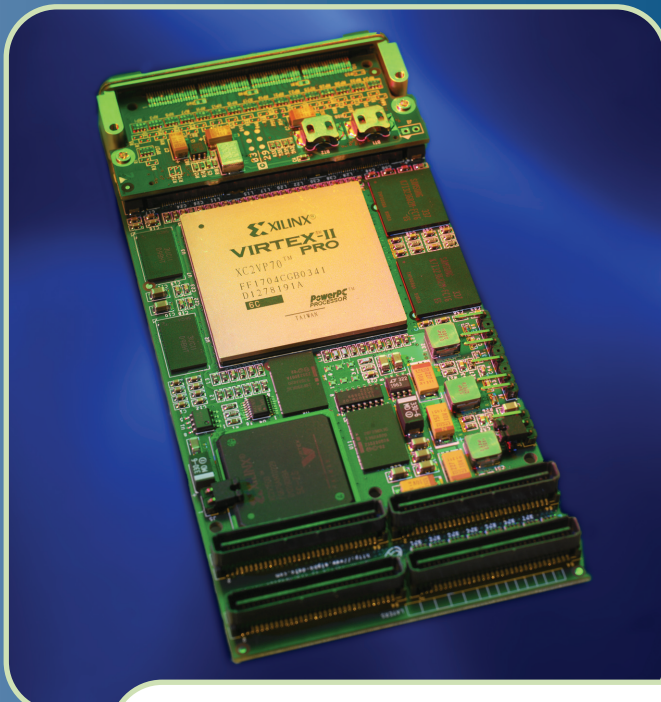


XILINX VIRTEX-II Pro RECONFIGURABLE COMPUTER

Features



Xilinx Virtex-II Pro based PMC ideal for DSP and Reconfigurable Computing

Support for the largest Xilinx devices from 2VP70 to 2VP100

4 banks of DDR SSRAM, 2 banks of DDR SDRAM and 2 banks of Flash

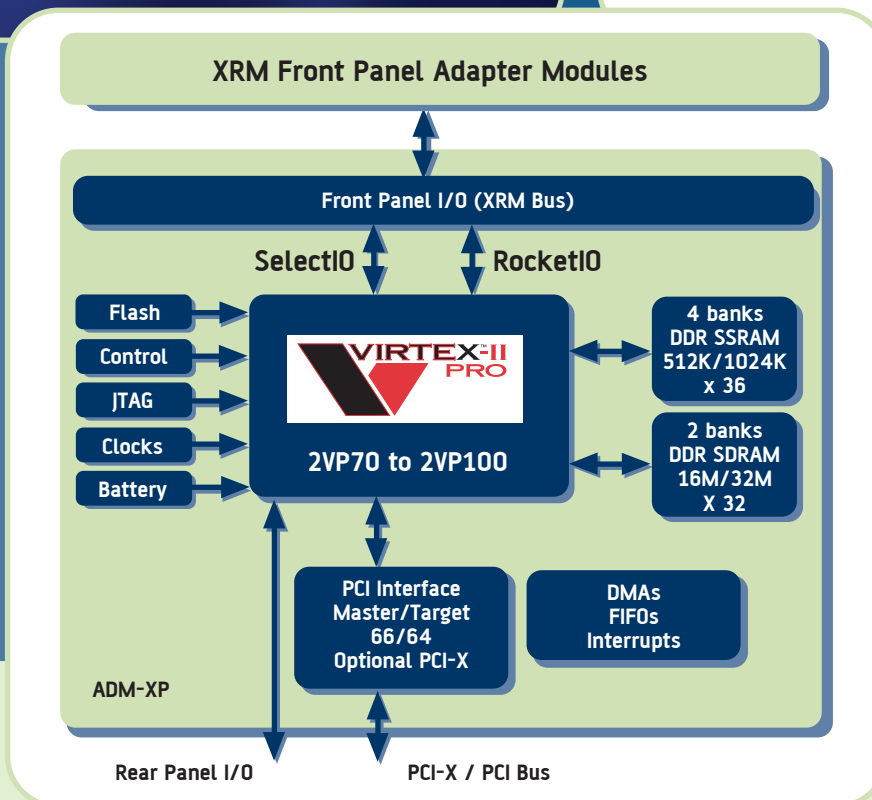
133MHz PCI-X or 66MHz PCI host interface with 2 DMA channels

Flexible front and rear panel I/O interface options, bandwidth up to 5GBytes/sec

Drivers for Windows, VxWorks and Linux. Linux and VxWorks BSP for embedded PowerPC

Support for System Generator, EDK and Handel-C

Common API for easy cross platform development – source code available



**XILINX VIRTEX-II Pro RECONFIGURABLE COMPUTER**
**Specifications**

		<b>ADM-XP</b>
<b>Virtex-II Pro FPGA</b>		2VP70, 2VP100, 2VP125
<b>PCI Bus</b>		133MHz PCI-X or PCI 64-bit 66MHz
<b>FPGA Local Bus</b>		64-bit 80MHz
<b>Memory</b>	<b>DRAM</b>	2 independent DDR SDRAM banks maximum configuration 256MByte
	<b>SRAM</b>	4 independent DDR SSRAM banks maximum configuration 16MByte
	<b>Flash</b>	2 banks 16 MByte
<b>I/O</b>		XRM Bus, Pn4 connector
<b>Configuration</b>		SelectMap, Flash, JTAG header
<b>Software Development Kit</b>	<b>Windows</b>	WinNT, XP, 2000
	<b>Linux</b>	Red Hat, Hard Hat
	<b>VxWorks</b>	

**XRM-Adapters**

XRM-I0146	High Density LVDS/LVTTL
XRM-I034	Low Density LVDS/LVTTL
XRM-MGT	RocketIO/High Density LVDS/LVTTL
XRM-DDR	1 bank DDR SDRAM, LVDS/LVTTL
XRM-ZBT	2 banks ZBT SSRAM, RS-232
XRM-FPDP	Front Panel Data Port
XRM-ADC	1/2 ch 105MSPS 14-bit ADC
XRM-DAC	2 ch 105MHz 14-bit DAC
XRM-ETH	Ethernet, LDVS/LVTTL, RS-232
XRM-RIO	RapidIO
XRM-CLINK	CameraLink
XRM-HSSDC2	RocketIO

**Ordering Information**
**ADM-XP/xxx-y/s/d(xrm)**

<b>xxx</b>	<b>Virtex-II Pro size</b>	70, 100 or 125
<b>y</b>	<b>Virtex-II Pro speed</b>	5, 6 or 7
<b>s</b>	<b>DDR SSRAM (MB)</b>	8 or 16
<b>d</b>	<b>DDR SDRAM (MB)</b>	128 or 256
<b>xrm</b>	<b>XRM front panel adapter</b>	