

Summary

The **ADM-XRC-5T1** is a high performance reconfigurable PMC (PCI Mezzanine Card) based on the Xilinx Virtex-5 LXT, FXT and SXT range of Platform FPGAs.

Features include high speed PCI-X interface, external memory, high density I/O, programmable clocks, temperature monitoring, battery backed encryption (by using an appropriate XRM) and flash boot facilities.

A comprehensive cross platform

API with support for **Microsoft Windows, Linux** and

VxWorks

provides access to the full functionality of these hardware features.

Features

Applications

JPEG2000
 Radar/Sonar Beamforming
 ELINT
 Image/Video Processing
 Data Encryption

Target Device(s):

Xilinx V5: LX110T, LX155T, FX70T, FX100T, SX95T (FFG1136)

Memory:

SDRAM - Up to 1GByte in 2 independent banks of DDR-II SDRAM @ 333MHz

FLASH - 4MByte serial Flash

FLASH - Configuration Flash providing an initialisation design for automatic loading into the target FPGA.

Front I/O:

Up to 146 LVTTTL/LVDS I/O

Programmable signaling levels of 1.8V, 2.5V or 3.3V

8 High-Speed Serial Links

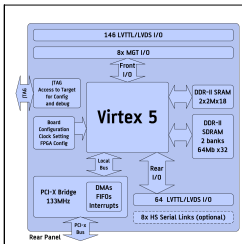
Rear I/O:

64 I/O connections via PMC Pn4 connector

Programmable signaling levels of 2.5V or 3.3V

8 High-Speed Serial Links via P15 connector

Only if the "extra connector fitted" option is chosen



Specification

Product Name	ADM-XRC-5T1
Target Device(s)	Xilinx V5 LX110T, LX155T, FX70T, FX100T, SX95T (FFG1136)
Interface	PCI/PCI-X
Memory	SDRAM - Up to 1GByte in 2 independent banks of DDR-II SDRAM @ 333MHz FLASH - 4MByte serial Flash FLASH - Configuration Flash providing an initialisation design for automatic loading into the target FPGA.
Front I/O	Up to 146 LVTTTL/LVDS I/O Programmable signaling levels of 1.8V, 2.5V or 3.3V 8 High-Speed Serial Links
Rear I/O	64 I/O connections via PMC Pn4 connector Programmable signaling levels of 2.5V or 3.3V 8 High-Speed Serial Links via P15 connector Only if the "extra connector fitted" option is chosen
Special Features	Option of P15 connector (Pseudo-XMC) providing High Speed Serial link to the carrier
Clocks	Local bus clock programmable up to 80MHz Low-jitter user clock, programmable up to 637.5MHz Custom clock inputs available through the XRM interface Additional 200MHz reference clock for IOB delay circuits.
Device Configuration	PCI Bus direct to SelectMAP port From Flash direct on power up External JTAG connector
Software	Drivers for Microsoft Windows, Linux and VxWorks. API with template designs in VHDL and Verilog
Environmental	Temperature: Air cooled industrial option (AC1): Operating Temperature -40° to +70°C EMC: FCC 47CFR Part 2 EN55022 Equipment Class B RoHS Compliance: All standard products are RoHS compliant. Please contact Alpha Data Sales for details of Tin/Lead build options.

Ordering Code
ADM-XRC-5T1/z-y(m)(c)(x)

Virtex-5 device	z	IGNORE
Virtex-5 speed	y	1, 2, 3
Memory Size Fitted	m	blank = each bank has 256MByte - 512MByte for the card, /1 = each bank has 512MByte - 1GByte for the card
HS Serial Connector Fitted	x	blank = not fitted, /X = Connector fitted
Air/Conduction cooled	c	blank = air cooled commercial, /AC1 = air cooled industrial

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