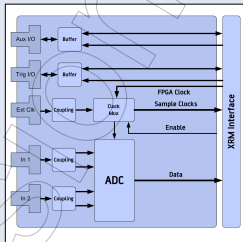

Summary

The **XRM-ADC-D7-500** is a dual channel 500MHz sampling Analogue to Digital converter module for use with Alpha Data FPGA systems. The XRM-ADC-D7-500 provides two ac-coupled channels of analogue to digital conversion with 12 bit resolution and supports sampling rates up to 500 MHz . It is aimed at applications such as IF/Baseband Signal Sampling.

Features
Front Connector I/O:

- Dual 12-bit ADC up to 500Mps
- External Clock Input
- Trigger I/O port
- Auxiliary I/O port





Specification

Product Name	XRM-ADC-D7-500
Host I/F	N/A
Front I/O	<p>Dual Channel ADC: Dual 12-bit ADC up to 500Mpsps resolution = 12-bit levels = +12 dBm (2.5 Vppk =ADC full scale) fmax = 500Mpsps bandwidth = 4.5 MHz to 700 MHz (3 dB) connector = SMA SMA-L SMB SMC</p> <p>External Clock: External Clock Input impedance = 50 AC levels = 0 dBm nominal (640 mV pk to pk), +12 dBm maximum (2.5 V pk to pk)</p> <p>Trigger I/O: Trigger I/O port type= "I/O" channels = 1 levels = +3V3 LVTTTL : 4k7 Ohms, dc coupled</p> <p>Auxiliary I/O: Auxiliary I/O port type= "I/O" channels = 1 levels = +3V3 LVTTTL : 4k7 Ohms, dc coupledL</p>
Software	Example UCF, HDL files and Application software are provided with the board.
Environmental	<p>Temperature: Operating Temperature 0° to +55°C</p> <p>EMC: FCC 47CFR Part 2 EN55022 Equipment Class B</p>

Ordering Codes

XRM(xver)-ADC-D7-500(con)		
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)
Connector Option	con	/blank=SMA(7mm standard), /SMA20=Long Barrel SMA(20mm), /SMB, /SMC

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