
**Summary**

The **XRM-DDR** is an I/O Module which provides extra DDR memory and external discrete digital I/O interface. The **XRM-DDR** is suitable for mating with our FPGA based cards. The XRM-DDR provides either 128MB, 256MB or 512MB DDR SDRAM directly accessible by the FPGA. The XRM-DDR also features a 38 way Micror I/O cable header.

**Features**
**Applications:**

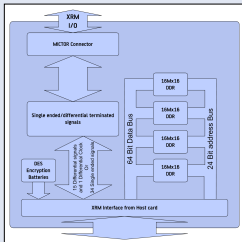
- Extended memory storage for developing applications
- Simple I/O interface

**Memory:**

- DDR** - Provides between 128MBytes and 512MBytes.

**Front Connector I/O:**

- 34 discrete digital I/Os



**Specification**

Product Name	XRM-DDR
Memory	<b>DDR</b> - Provides between 128MBytes and 512MBytes.
Front I/O	<b>Digital I/O:</b> - 34 discrete digital I/Os levels=LVTTTL connector=Micro connectorconfigurable as 16 LVDS pairs with LVDS clock.
Software	Example UCF, HDL files and Application software are provided with the board.
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.
Environmental	<b>Temperature:</b> Operating Temperature 0° to +55°C  <b>EMC:</b> FCC 47CFR Part 2 EN55022 Equipment Class B

**Ordering Codes**
**XRM-DDR(m)jss**

Memory Fitted	m	/128 = 128MBytes, /256 = 256MBytes, /512 = 512MBytes
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