



## IF/Baseband Signal Sampling

## Board Features

- Dual 14-bit 125Mps ADCs
- External Clock Input

## Summary

The **XRM2-ADC-D2/125** is an XRM2 I/O Module, providing two Analog to Digital converters with 14-bit resolution and sampling rates up to 125Mps.

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. A Trigger I/O port is provided for use as a trigger control and an Auxiliary I/O port for general purpose signaling.

A number of customisation options are offered with this card, ranging from signal input connector style through to transformer or DC-Coupling of inputs.

## Deliverables

XRM2-ADC-D2/125 Board  
 One Year Warranty  
 One Year Technical Support

## Input/Output Interfaces

### 2x Dual Analog to Digital Converters

Resolution: 14-bit  
 Max Sample Freq: 125Mpsps  
 Bandwidth: AC Coupled = 10Hz to 200MHz  
 DC Coupled = DC to 200MHz  
 Impedance: 50Ω  
 Connector: SMA

### 1x External Clock Input

Impedance: 50Ω (AC coupled)  
 Levels: +120Bm nominal (2.5V ptk to ptk)  
 Connector: SMA

### 1x Trigger I/O

Impedance: Input: 4k7Ω (DC Coupled)  
 Levels: +3V3 LVTTTL  
 Connector: SMA

### 1x Auxiliary I/O

Impedance: Input: 4k7Ω (DC Coupled)  
 Levels: +3V3 LVTTTL  
 Connector: SMA



**Support**

Example UCF, HDL files and Application software are provided with the board.

**Environmental Specification**
**Temperature Ranges**

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	55°C	-40°C	85°C

Operating Humidity : Up to 95% (non-condensing)

**EMC Standards**

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

**Ordering Information**

**Order Code: XRM2-ADC-D2/125(coupling)**

Option	Code	Description of Options
Signal Coupling	coupling	Blank = AC Coupled, /DC = DC Coupled

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