

Summary

The XRM2-ADC-D9 is an I/O Module which provides two Analog to Digital converters with 14-bit resolution at sampling rates up to 500MHz. The XRM is aimed at IF/Baseband Signal Sampling. An external clock source may be used or an internally generated clock can be used to provide the sampling clock. An Auxiliary I/O port is provided for use as a trigger input and general purpose signaling. An additional two ports are available for use as high-speed interconnect between boards for synchronisation.

Features

Applications

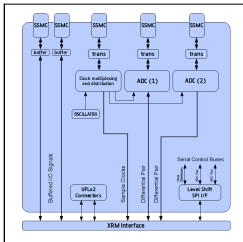
IF/Baseband Signal Sampling

Front I/O:

Dual Channel 14-bit ADC - 500Mbps (SSMC)

Clock In (SSMC)

Auxiliary I/O port (SSMC)



Specification

Product Name	XRM2-ADC-D9
Front I/O	<p>: Dual Channel ADC: Fmax = 500Mbps Resolution = 14-bit Bandwidth = 4.5MHz to 700MHz Levels = +10dBm Impedance = 50 Ohm Connector = SSMC</p> <p>Note: exceeding the maximum signal limit may result in permanent degradation of converter performance.</p> <p>: Single Channel Clock: Fclock = Fmax Levels = 3v3 Connector = SSMC</p> <p>Note: Exceeding the maximum voltage limit may result in permanent degradation of converter</p> <p>: Dual Channel I/O: Levels = 3v3 Connector = SSMC</p> <p>Note: Exceeding the maximum voltage limit may result in permanent degradation of converter</p>
Software	Example UCF, HDL files and Application software are provided with the board.
Environmental	<p>Temperature: Air cooled option (AC0): Operating Temperature 0° to +55°C</p> <p>EMC: FCC 47CFR Part 2 EN55022 Equipment Class B</p> <p>RoHS Compliance: All standard products are RoHS compliant. Please contact Alpha Data Sales for details of Tin/Lead build options.</p>

Ordering Code
XRM2-ADC-D9

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