


IF/Baseband data signal generation
Board Features

- Dual 14-bit 275Msps DACs
- External Clock Input

Summary

The **XRM2-DAC-D3/275** is an XRM2 I/O Module which provides two 14-bit Digital-to-Analog converters sampling at up to 275Msps.

Aimed at IF/Baseband signal generation, provision is made for use of either an external or internally generated clock source. External trigger and auxiliary I/O ports are also provided.

Deliverables

XRM2-DAC-D3/275 Board
 One Year Warranty
 One Year Technical Support

Input/Output Interfaces
2x Dual 14-bit DACs up to 275Msps

Resolution: 14-bit
 Max Sample Freq: 275MHz
 Bandwidth: AC Coupled = 0.25MHz to 80MHz
 DC Coupled = DC to 80MHz
 Impedance: 50Ω
 Connector: SMA

1x External Clock Input

Impedance: 50Ω AC
 Levels: +12dBm nominal (2.5V pk to pk)
 Connector: SMA

1x Trigger I/O port

Impedance: 4k7Ω (DC coupled)
 Levels: +3V3 LVTTTL
 Connector: SMA

1x Auxiliary I/O port

Impedance: 4k7Ω (DC coupled)
 Levels: +3V3 LVTTTL
 Connector: SMA



Support

Example UCF, HDL files and Application software are provided with the board.

Environmental Specification
Temperature Ranges

| Cooling Option | Operating Temperatures | | Storage Temperatures | |
|----------------|------------------------|------|----------------------|------|
| | Min | Max | Min | Max |
| AC0 | 0°C | 55°C | -40°C | 85°C |

Operating Humidity : Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

Ordering Information

Order Code: XRM2-DAC-D3/275(coupling)(io)

| Option | Code | Description of Options |
|--------------------|----------|--|
| Output Coupling | coupling | blank = DC Coupled (standard), /AC = AC Coupled |
| I/O Voltage Option | io | blank = LVTTTL I/O levels, /5V = TTL I/O Levels |

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