



General purpose I/O Interface

Board Features

- 146 Digital I/O

Summary

The **XRM2-IO146** is an XRM2 I/O Module which provides up to 146 LVDS/LVTTL I/O via a Microwire 152pin connector.

The XRM2-IO146 also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.

Deliverables

XRM2-IO146 Board
 One Year Warranty
 One Year Technical Support

Input/Output Interfaces

66x differential pairs

Levels: LVDS or LVTTL I/O
 Connector: Microwire 152 pin

10x Single ended I/O

Levels: LVDS or LVTTL I/O
 Connector: Microwire 152 pin

4x Single ended clock pins

Levels: LVDS or LVTTL I/O
 Connector: Microwire 152 pin



Support

Example UCF, HDL files and Application software are provided with the board.

Environmental Specification
Temperature Ranges

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	55°C	-40°C	85°C

Operating Humidity : Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

Ordering Information

Order Code: XRM2-IO146(LOCK)(EXTRA)

Option	Code	Description of Options
Custom Modes	EXTRA	blank=No other options, {code}=Other build options for custom products
Cable Lock	LOCK	blank=No locking mechanism, {LOCK}=Cable locking assembly fitted
NOTE	If the XRM2-IO146 is to be used to supply 5V indicate this in your order so the appropriate components can be fitted.	

Address: 160 Dundee Street, Suite 4A,
Edinburgh, EH11 1DQ, UK
Telephone: +44 131 558 2600
Fax: +44 131 558 2700
email: sales@alpha-data.com
website: http://www.alpha-data.com

Address: 611 Corporate Circle Suite H
Golden, CO 80401
Telephone: (303) 954 8768
Fax: (866) 820 9956 - toll free
email: sales@alpha-data.com
website: http://www.alpha-data.com