

## **ADA-PCIE-CLINK6V**

Datasheet Revision: Draft

2nd January 2013



## Summar

The ADA-PCIE-CLINKEV provides an out of the box FPGA based PCIe frame grabber. Included in the kit is the source code for the FPGA designs and software, so that they may be customised to suit the exact needs of the end application.

The provided FPGA designs implement a simple frame grabber compatible with acquiring data from Camera Lurk camera(s). These designs can be used as a starting control of the starting frame of the starting frame framework of the starting frame framework of the starting frame framework of the starting fram

## Features Applications:

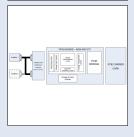
FPGA frame grabber Video Stream Processor

Target Devices: Xilinx Virtex-6: SX315T (FFG1759)

Memory:

SDRAM - 2GByte in 4 independent banks of DDR3 SDRAM @ 800MT/s (32-bit wide so 3.2GB/s) Front Connector I/O:

Full Cameralink interface





## ADA-PCIE-CLINK6V



Specification	
Product Name	ADA-PCIE-CLINK6V
Target Devices	Xilinx Virtex-6 - SX315T (FFG1759)
Host I/F	PCI EXPRESS
Memory	SDRAM - 2GByte in 4 independent banks of DDR3 SDRAM @ 800MT/s (32-bit wide so 3.2GB/s)
Front I/O	Camera Link Interface: Full Cameralink interface connector = 2xminiMDR-26
Special Functions	ADM-XRC-6T1 with Virtex-6 SX315 FPGA and 2.0 GiBytes of DDR3 XRM-CLINIK-MINI to provide Camera Link 10 to FPGA ADC-PCIE-XMC PCIE Gen 2.0 carrier for ADM-XRC-6T1
Software	API including source code for FPGA based frame grabber Example application for capturing images, serial communications with camera(s), and controlling camera(s) CC lines FPGA frame grabber source code
Environmental	Temperature: Air cooled golder,

Ordering Codes
ADA-PCIE-CLINK6V

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