

## XRM-CLINK-MINI



19th July 2016



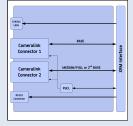
Summary

The XRM-CLINK-MINI is an I/O Module which provides an industry standard CameraLink interface.

I provides the user with the ability to implement computationally-intensive applications such as them groutbers, digital video communications and image processing systems in the PFGA without surging a computation camera in the TRAMC LURAW MILLOR out Region that if the TRAMC LURAW MILLOR out Region that if the TRAMC LURAW MILLOR out Region that if the TRAMC LURAW MILLOR out region to the TRAMC LURAW MILLOR out the TRAMC LURAW MILLOR OUT TO THE ADMINISTRATION OF THE ADMINISTRATION

etc as required.









Specification			
Product Name	XRM-CLINK-MINI		
Front I/O	Single Channel Cameralink: Dual Channel Cameralink:		
Software	Example UCF, HDL files and Application software are provided with the board.		
Environmental	Temperature: Air cooled option (ACP) Cyberating Temperature 0" to +50°C  BMC. CYPTR PINT. BHSDZ 2 (Explaned Class B  RABS Compliance: Air standard products are RAPS compliant. Please contact Alpha Data Sales for details of Tin-Lead build options.		

Ordering Code				
XRM(xver)-CLINK-MINI(type)(p)				
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)		
Туре	type	-FULL = FULL 2 x SDR, -OB = Dual BASE 2 x SDR		
PoCL	Р	BLANK = No PoCL fitted, /POCL - PoCL Fitted		

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