

XRM-HSSDC2A



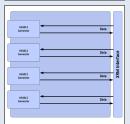
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Summar

The XRM-HSSDC2A is an I/O Module which provides 4 HSSDC2 high speed serial connectors. Allows the High Speed Serial IO on the FPGA board to drive signals through HSSDC2 connectors to external equipment.

Features Applications: High Speed Serial Communications Front Connector I/O: Quad High Speed serial links (up to 2.5Gbps)





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Specification				
Product Name XRM-HSSDC2A				
Front I/O High Speed Serial Link: - Quad High Speed serial links (up to 2.5Gbps) connector=HSSDC2 fmax=2.5Gbps				
Software	No Software Required			
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.			
Environmental	Temperature: Operature: Operature			

П	Ordering Codes				
Γ	XRM(xver)-HSSDC2A				
	XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)		

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