

## XRM-I014

## Datasheet Revision: 1.1

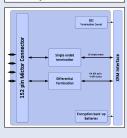
## 6th February 2014



## Summa

The XRM-IO146 is an I/O Module which provides up to 146 LVDS/LVTTL I/O via the Mictor 152pin connector.The XRM also provides battery support to

provide a back-up supply for encryption key storage on the connected FPGA board. Features
Applications:
General purpose I/O Interface
Front Connector I/O:
146 discrete digital I/Os LVOS/LVTTL









Specification			
Product Name	XRM-IO146		
Front I/O	Digital I/O: 146 discrete digital I/Os LVDS/LVTTL levels = LVDS or IVTTL I/O connector = Mictor 152 pin 64 differential pairs, 10 single ended signals and 4 differential clocks		
XRM2	The XRM-IO146 is also available for XRM2 based FPGA products.		
Software	Example UCF, HDL files and Application software are provided with the board.		
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.		
Environmental	Temperature: Operating Temperature 0' to +55°C  EMC: FDC 47CFR Part 2  FESSIZIZ Equipment Class B		

Ordering Codes			
XRM(xvar)-I/O146(B)(D)(LOCK)(EXTRA)			
XRM Version X0		blank=Original XRM (FPGA products up to Vintex-5), 2=XRM Version 2 (FPGA products Vintex-6 and later)	
For Virtex-4 products		blank=Virtex-5 and later products, A=For Virtex-4 products	
DCI Enabled for Virtex-4 and earlier products		blank+Not enabled, -G=DCI enabled for Virtex-4 and earlier products	
Custom Modes		blank-No other options, -{code}=Other build options for custom products	
Cable Lock		blank-No locking mechanism, -LOCK=Cable locking assembly fitted	
NOTE		When using with a Vintex-4 LX or SX based board order the XRM-IO146A. Check with ??mailto: sales@alpha-data.com-Sales?? for further information.	
NOTE #		When using with a Virtex-6 based board order the XRM2-IO146.	
NOTE		If the XRM-IO146 is to be used to supply 5V indicate this in your order so the appropriate components can be fitted.	

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