

XRM-ZBT

Datasheet Revision: 1.0

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Summary

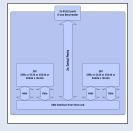
The XRM-ZBT is an I/O Module which provides extra ZBT memory. The XRM-ZBT is suitable for mating with some of our FPGA based cards. The XRM-ZBT provides 2 banks of between 256K and 2048K x 36-bit ZBT pipelined memory, directly accessible by the FPGA. The XRM-ZBT also leatures 2 RS232 ports on the front panel.

Applications:

Extended memory storage for developing applications Simple I/O interface

ZBT - Provides 2 independent banks of between 256K and 2048K x 36-bit ZBT pipelined memory. Front Connector I/O:

Dual RS232 Interfaces





XRM-ZBT



Specification			
Product Name	XRM-ZBT		
Memory	ZBT - Provides 2 independent banks of between 256K and 2048K x 36-bit ZBT pipelined memory.		
Front I/O	R\$232: - Dual R\$232 Interfaces		
Software	ZBT IP core allows simple integration with the FPGA board.		
Battery	The XRM provides holders for two back-up batteries. Uses SR60 or equivalent batteries.		
Environmental	Temperature: Operating Temperature 0* to +55°C EMC: FCC 47CFR Part 2 FMS0022 Equipment Class B		

Ordering Codes				
XRM(xver)-ZBT(m)				
XRM Version	xver	blank=Original XRM (FPGA products up to Virtex-5), 2=XRM Version 2 (FPGA products Virtex-6 and later)		
Memory Fitted	m	/2 = 256K x 36-bit, /4 = 512K x 36-bit, /8 = 1024K x 36-bit, /16 = 2048K x 36-bit		
NOTE	#	Please contact ??mailto:sales@alpha-data.com~Alpha Data Sales?? for use of the XRM with Virtex-5 devices		

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