XRM2-ADC-D7/500

Datasheet Revision: 1.0
9th April 2019

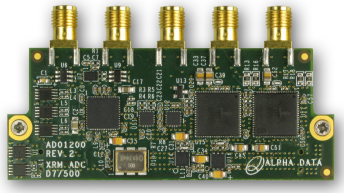
AD01200

Prime Application

IF/Baseband Signal Sampling

Board Features

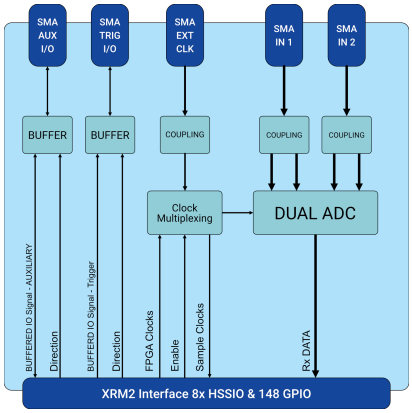
- Dual 12-bit 500Msps ADCs
- External Clock Input



Summary

The **XRM2-ADC-D7/500** is an XRM2 I/O Module, providing two Analog to Digital converters with 12-bit resolution and sampling rates up to 500Msps

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. A Trigger I/O port is provided for use as a trigger control and an Auxiliary I/O port for general purpose signaling. An additional two ports are available for use as high-speed interconnect between boards for synchronisation.



Deliverables

XRM2-ADC-D7/500 Board
One Year Warranty
One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Input/Output Interfaces

ADC
Dual Analog to Digital Converters

Resolution: 12-bit
Max Sample Freq: 500Msps
Bandwidth: 4.5 MHz to 700 MHz (3 dB)
Impedance: 50Ω
Connector: SMA

External clock input
External Clock Input

Trigger I/O
Trigger I/O

Auxiliary I/O
Auxiliary I/O

Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	55°C	-40°C	85°C

Operating Humidity

Up to 95% (non-condensing)

EMC Standards

FCC 47CFR Part 2
EN55022:2010 Equipment ClassB

Ordering Information

Order Code: XRM2-ADC-D7/500

ALPHA DATA
www.alpha-data.com

UK Office
Address: Suite L4A, 160 Dundee Street, Edinburgh, EH11 1DQ, UK
Telephone: +44 131 558 2600
Fax: +44 131 558 2700
email: sales@alpha-data.com

USA Office
611 Corporate Circle, Suite H
Golden, CO 80401
(303) 954 8768
(866) 820 9956 - toll free
sales@alpha-data.com