

AD01105

### Prime Application

General purpose I/O Interface

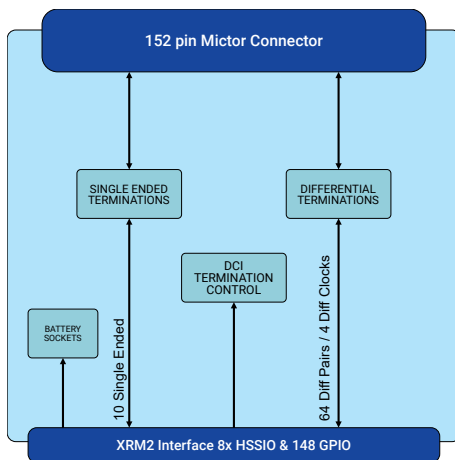
### Board Features

- 146 Digital I/O

### Summary

The **XRM2-IO146** is an XRM2 I/O Module which provides up to 146 LVDS/LVTTL I/O via a Mictor 152pin connector.

The XRM2-IO146 also provides battery support to provide a back-up supply for encryption key storage on the connected FPGA board.



### Deliverables

XRM2-IO146 Board  
One Year Warranty  
One Year Technical Support

### Board Format

Alpha Data XRM2 I/O Module

### Input/Output Interfaces

#### Digital I/O

differential pairs  
Single ended I/O  
Single ended clock pins

### Environmental Specification

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC0	0°C	+55°C	-40°C	+85°C

### Operating Humidity

Up to 95% (non-condensing)

### EMC Standards

FCC 47CFR Part 2  
EN55022:2010 Equipment ClassB

### Ordering Information

**Order Code: XRM2-IO146(LOCK)(EXTRA)**

Option	Code	Description of Options
Custom Modes	EXTRA	blank=No other options, -[code]=Other build options for custom products
Cable Lock	LOCK	blank=No locking mechanism, -LOCK=Cable locking assembly fitted
NOTE		If the XRM2-IO146 is to be used to supply 5V indicate this in your order so the appropriate components can be fitted.